JJMICROELECTRONICS

100V, 116A, 5.7mΩ N-channel Power SGT MOSFET

JMSH1006PE

Features

- Excellent $R_{DS(ON)}$ and Low Gate Charge
- 100% UIS Tested
- 100% ΔVds Tested
- Halogen-free; RoHS-compliant

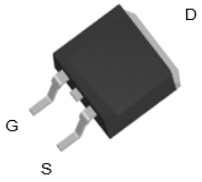
Applications

- Load Switch
- PWM Application
- Power Management

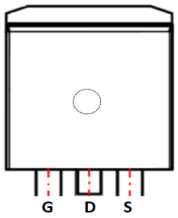
Product Summary

Parameters	Value	Unit
V_{DSS}	100	V
$V_{GS(th)\_Typ}$	3.2	V
I_D(@V_{GS}=10V)	116	A
$R_{DS(ON)\_Typ}$(@V_{GS}=10V	5.7	mΩ

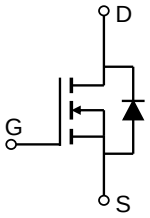
RoHS



TO-263-3L Top View



Pin Assignment



Schematic Diagram

Ordering Information

Device	Marking	MSL	Form	Package	Reel(pcs)	Per Carton (pcs)
JMSH1006PE-13	SH1006P	3	Tape&Reel	TO-263-3L	800	4000

Absolute Maximum Ratings (@ T_C = 25°C unless otherwise specified)

Symbol	Parameter		Value	Unit
V_{DS}	Drain-to-Source Voltage		100	V
V_{GS}	Gate-to-Source Voltage		±20	V
I_D	Continuous Drain Current	T_C = 25°C	116	A
		T_C = 100°C	82	
I_{DM}	Pulsed Drain Current [1]		Refer to Fig.4	A
E_{AS}	Single Pulsed Avalanche Energy [2]		356	mJ
P_D	Power Dissipation	T_C = 25°C	221	W
		T_C = 100°C	88	
T_J, T_{STG}	Junction & Storage Temperature Range		-55 to 150	°C

Thermal Characteristics

Symbol	Parameter	Max	Unit
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient [3]	35	°C/W
$R_{\theta JC}$	Thermal Resistance, Junction to Case	0.6	

Electrical Characteristics (T_J = 25°C unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
Off Characteristics						
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	I_D = 250μA, V_{GS} = 0V	100	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	V_{DS} = 80V, V_{GS} = 0V	-	-	1.0	μA
I_{GSS}	Gate-Body Leakage Current	V_{DS} = 0V, V_{GS} = ±20V	-	-	±100	nA
On Characteristics						
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS} = V_{GS}, I_D = 250μA	2.2	3.2	4.1	V
$R_{DS(ON)}$	Static Drain-Source ON-Resistance(4)	V_{GS} = 10V, I_D = 20A	-	5.7	7.4	mΩ
Dynamic Characteristics						
R_g	Gate Resistance	f = 1MHz	-	0.6	-	Ω
C_{iss}	Input Capacitance	V_{GS} = 0V, V_{DS} = 50V, f = 1MHz	-	3144	-	pF
C_{oss}	Output Capacitance		-	470	-	pF
C_{rss}	Reverse Transfer Capacitance		-	19	-	pF
Q_g	Total Gate Charge	V_{GS} = 0 to 10V V_{DS} = 50V, I_D = 20A	-	51	-	nC
Q_{gs}	Gate Source Charge		-	17	-	nC
Q_{gd}	Gate Drain("Miller") Charge		-	12	-	nC
Switching Characteristics						
$t_{d(on)}$	Turn-On DelayTime	V_{GS} = 10V, V_{DD} = 50V I_D= 20A, R_{GEN} = 3Ω	-	15	-	ns
t_r	Turn-On Rise Time		-	18	-	ns
$t_{d(off)}$	Turn-Off DelayTime		-	27	-	ns
t_f	Turn-Off Fall Time		-	8.0	-	ns
Body Diode Characteristics						
I_S	Maximum Continuous Body Diode Forward Current		-	-	116	A
I_{SM}	Maximum Pulsed Body Diode Forward Current		-	-	464	A
V_{SD}	Body Diode Forward Voltage	V_{GS} = 0V, I_S = 20A	-		1.2	V
trr	Body Diode Reverse Recovery Time	I_F = 15A, di/dt = 100A/us	-	56	-	ns
Qrr	Body Diode Reverse Recovery Charge		-	106	-	nC

Notes:

1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.
2. E_{AS} condition: Starting T_J=25C, V_{DD}=50V, V_{GS}=10V, R_G=25ohm, L=3mH, I_{AS}=15.4A, V_{DD}=0V during time in avalanche.
3. $R_{\theta JA}$ is measured with the device mounted on a 1inch^2 pad of 2oz copper FR4 PCB.
4. Pulse Test: Pulse Width≤300μs, Duty Cycle≤0.5%.

Typical Performance Characteristics

Figure 1: Power De-rating

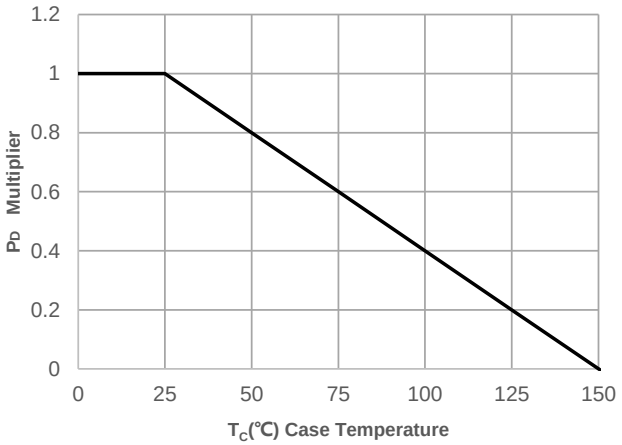


Figure 2: Current De-rating

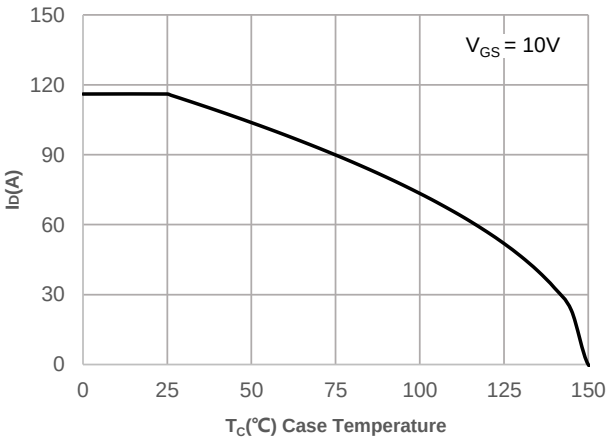


Figure 3: Normalized Maximum Transient Thermal Impedance

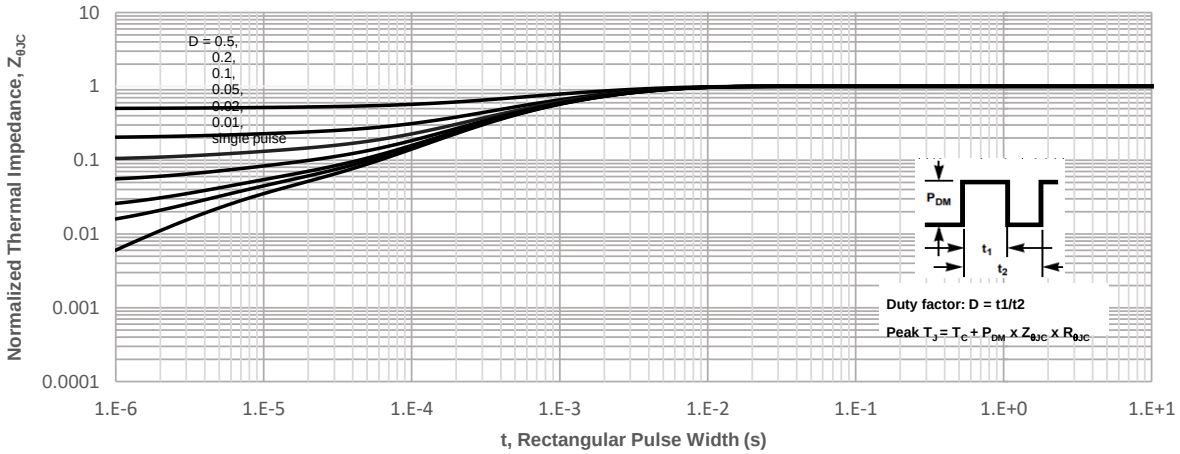
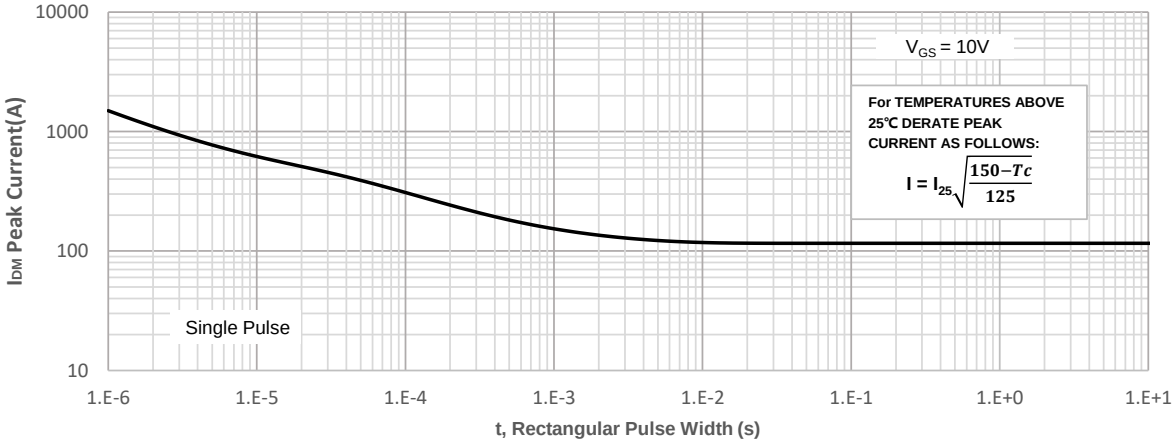


Figure 4: Peak Current Capacity



Typical Performance Characteristics

Figure 5: Output Characteristics

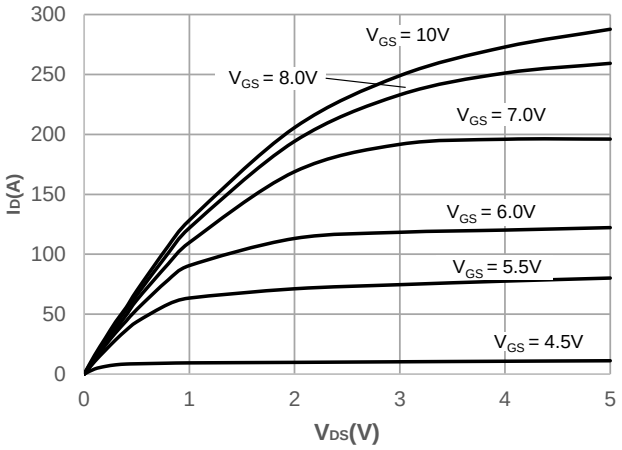


Figure 6: Typical Transfer Characteristics

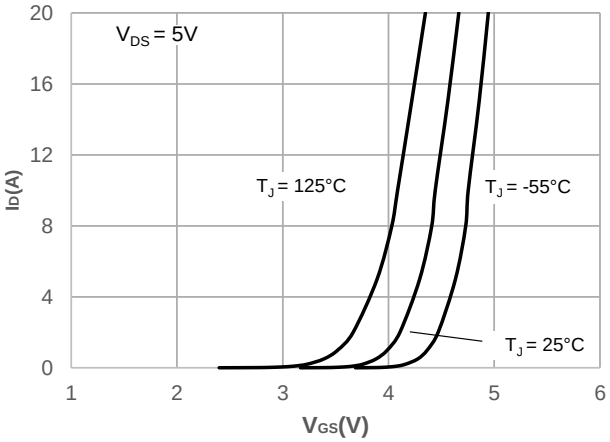


Figure 7: On-resistance vs. Drain Current

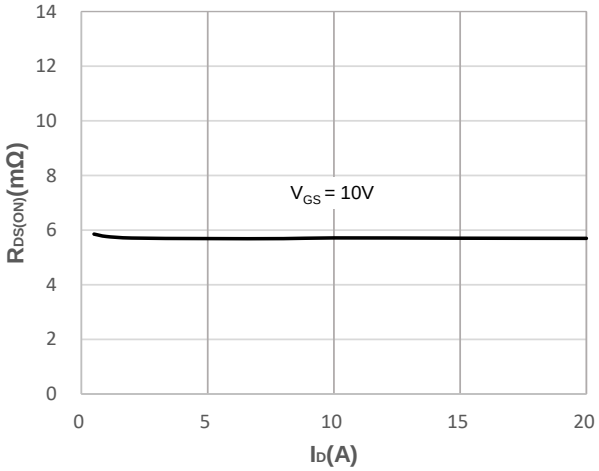


Figure 8: Body Diode Characteristics

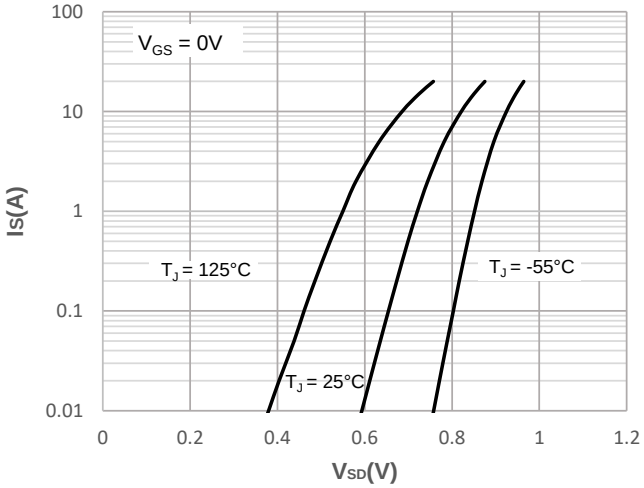


Figure 9: Gate Charge Characteristics

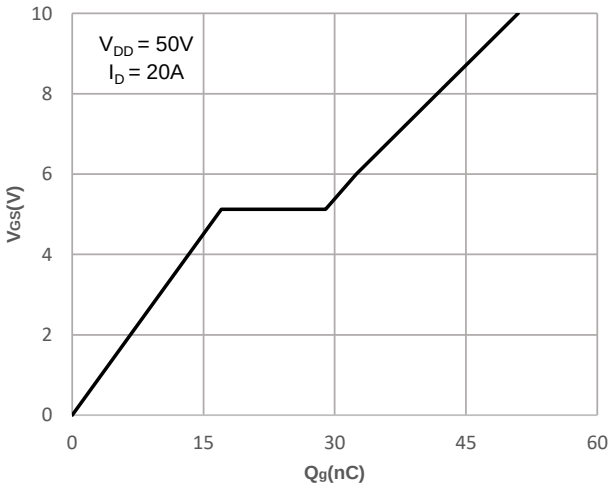
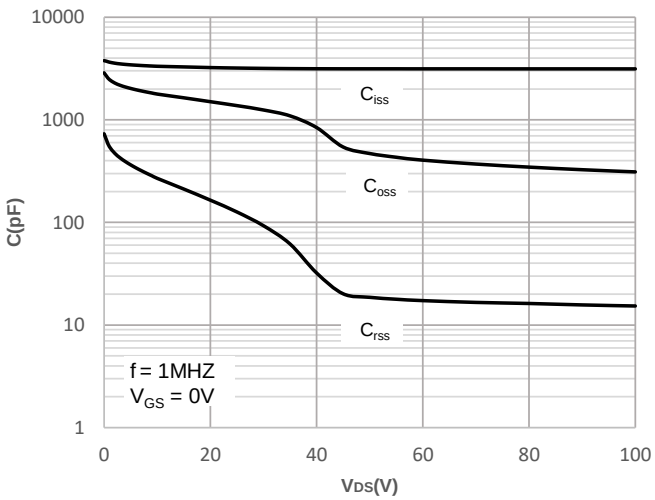


Figure 10: Capacitance Characteristics



Typical Performance Characteristics

Figure 11: Normalized Breakdown voltage vs. Junction Temperature

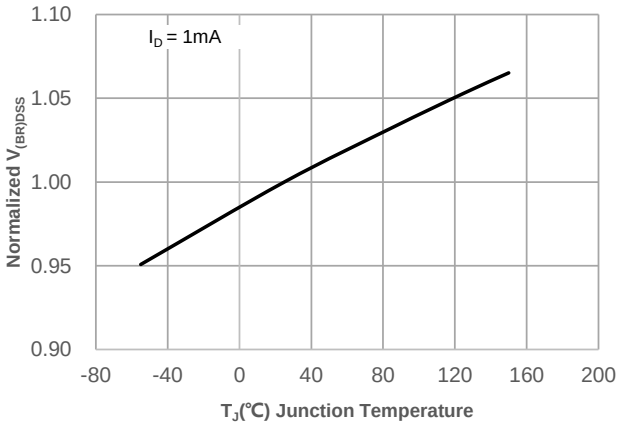


Figure 12: Normalized on Resistance vs. Junction Temperature

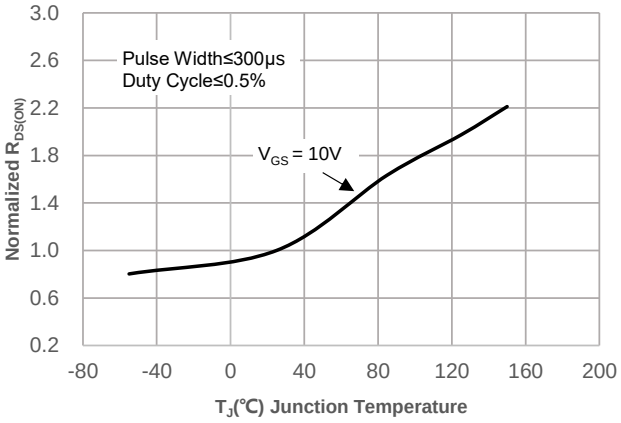


Figure 13: Normalized Threshold Voltage vs. Junction Temperature

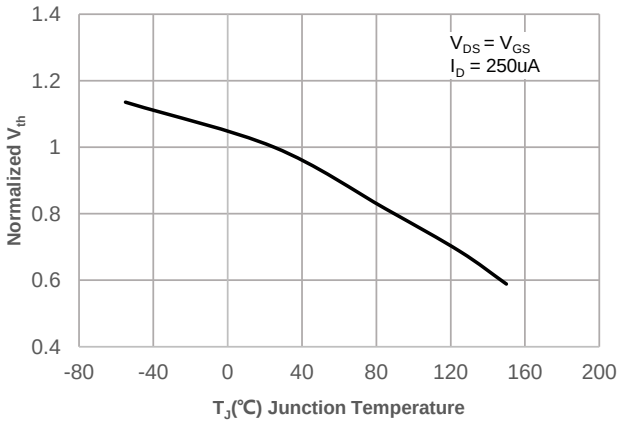


Figure 14: $R_{DS(ON)}$ vs. V_{GS}

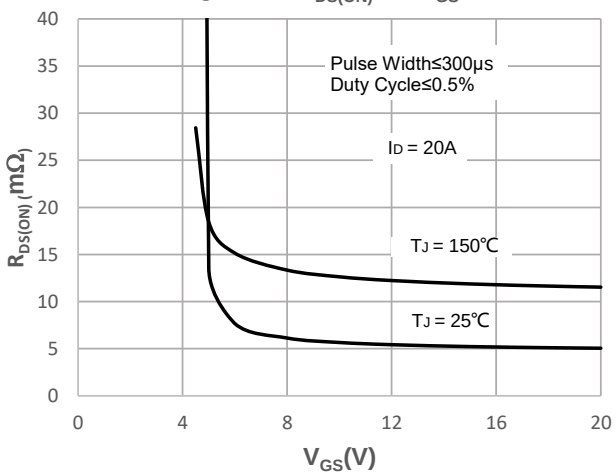
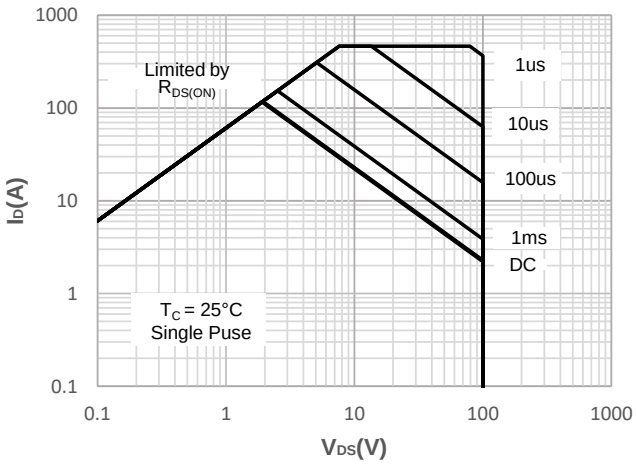


Figure 15: Maximum Safe Operating Area



Test Circuit

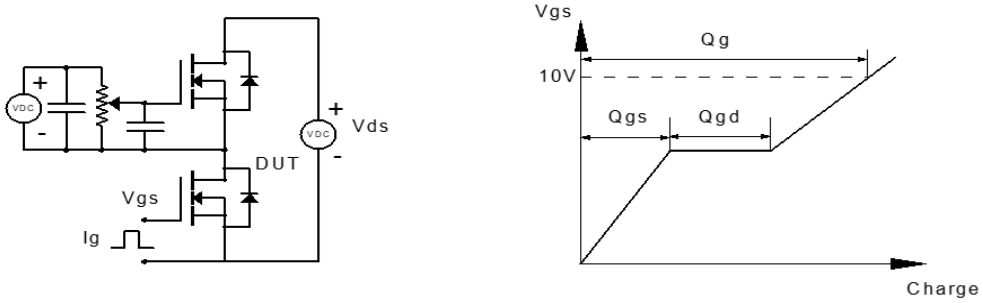


Figure 1: Gate Charge Test Circuit & Waveform

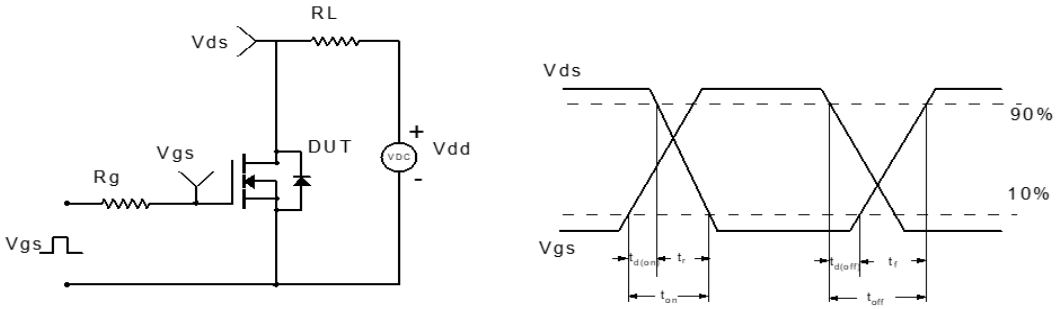


Figure 2: Resistive Switching Test Circuit & Waveform

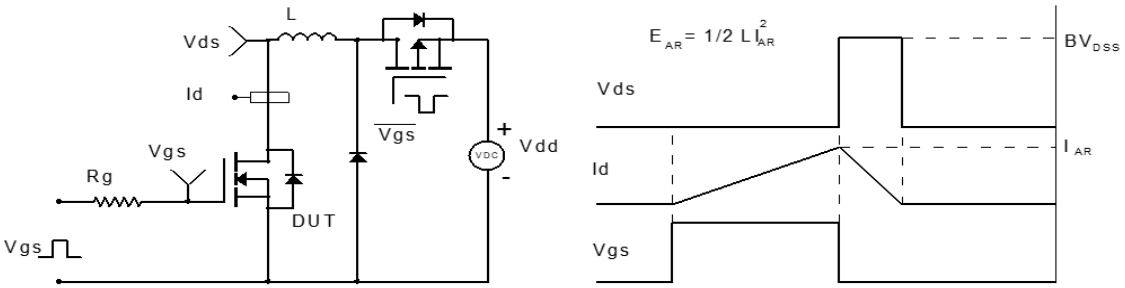


Figure 3: Unclamped Inductive Switching Test Circuit& Waveform

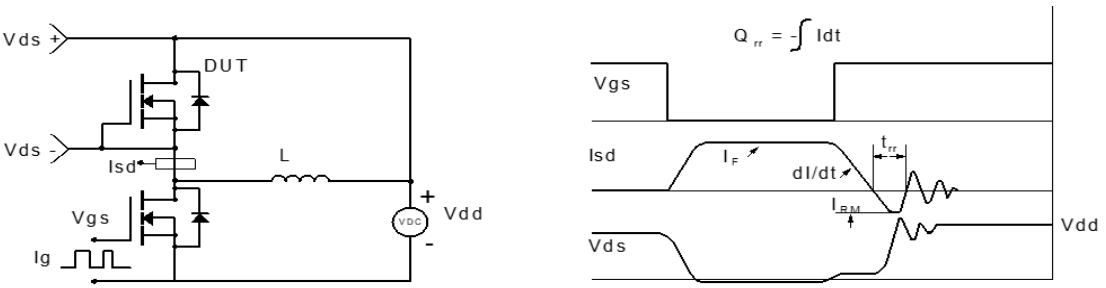
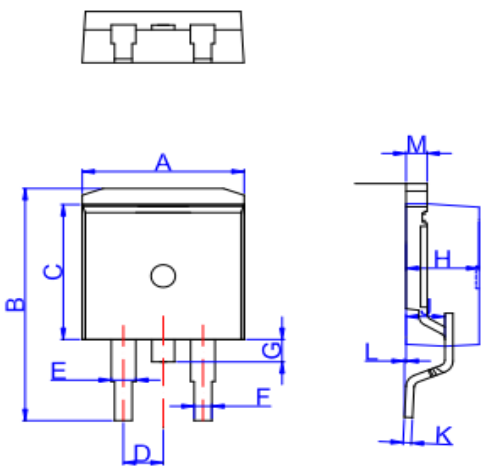


Figure 4: Diode Recovery Test Circuit & Waveform

Package Mechanical Data(TO-263-3L)



TO-263

Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	9.90		10.20	0.390		0.402
B	14.70		15.80	0.579		0.622
C	9.4		9.6	0.37		0.378
D		2.54			0.100	
E	1.20		1.40	0.047		0.055
F	0.75		0.85	0.029		0.033
G			1.75			0.069
H	4.40		4.70	0.173		0.185
J	2.30		2.70	0.091		0.106
K	0.38		0.55	0.015		0.022
L	0	0.10	0.25	0	0.004	0.010
M	1.25		1.35	0.049		0.053